Refine Search

Search Results -

Terms	Documents	
L16 same array	28	

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Database:

L17			Refine Search
		M	IXEIIIC OEGIGI
Recall Text 🗢	Clear		Interrupt

Search History

DATE: Friday, July 09, 2004 Printable Copy Create Case

Set Name side by side	= -	Hit Count	Set Name result set
DB=U	SPT,USOC,EPAB,JPAB; PLUR=YES;	OP = OR	
<u>L17</u>	L16 same array	28	<u>L17</u>
<u>L16</u>	L15 same redundant same memory	64	<u>L16</u>
<u>L15</u>	L14 same register	3504	<u>L15</u>
<u>L14</u>	column adj 1 address	16880	<u>L14</u>
<u>L13</u>	L12 and 19	21	<u>L13</u>
<u>L12</u>	register same address	84212	<u>L12</u>
<u>L11</u>	L9 and register	46	<u>L11</u>
<u>L10</u>	L9 and 11	0	<u>L10</u>
<u>L9</u>	redundant adj2 plane	106	<u>L9</u>
<u>L8</u>	L3 same column	59	<u>L8</u>
<u>L7</u>	L2 same column	128	<u>L7</u>
<u>L6</u>	L1 same plane	10	<u>L6</u>
<u>L5</u>	L4 and l3	5	<u>L5</u>
L4	bisr	318	L4

<u>L3</u>	L2 same array	148	<u>L3</u>
<u>L2</u>	L1 same memory	443	<u>L2</u>
<u>L1</u>	register same redundant same address	710	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents	
L12 same L7	5	

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
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Search:

L14

Database:

		Refine Search
Recall Text 👄	Clear	Interrupt

Search History

DATE: Friday, July 09, 2004 Printable Copy Create Case

<u>Set Name</u>	<u>Query</u>	Hit Count	Set Name
side by side			result set
DB=US	SPT; PLUR=YES; OP=OR		
<u>L14</u>	L12 same 17	5	<u>L14</u>
<u>L13</u>	L12 same 16	24	<u>L13</u>
<u>L12</u>	msb or lsb	17621	<u>L12</u>
<u>L11</u>	order adj l bit	11693	<u>L11</u>
<u>L10</u>	L9 and 18	4	<u>L10</u>
<u>L9</u>	storage adj 1 system	25031	<u>L9</u>
<u>L8</u>	L7 same memory	193	<u>L8</u>
<u>L7</u>	L6 same stor\$	368	<u>L7</u>
<u>L6</u>	partial adj2 address	806	<u>L6</u>
<u>L5</u>	clock adj 1 modifier	7	<u>L5</u>
<u>L4</u>	system adj1 clock adj1 modifier	0	<u>L4</u>
<u>L3</u>	L2 and register	1	<u>L3</u>
<u>L2</u>	L1 and cell	1	<u>L2</u>
<u>L1</u>	6393504[pn]	1	<u>L1</u>

END OF SEARCH HISTORY